DOE Centers of Excellence Performance Portability Meeting
April 18–21, 2016
Glendale, Arizona

Note: This is a high level agenda for early informational purposes; it will be updated once speakers have been identified.

Monday, April 18

5:30–8:00	Reception (drinks and finger food) and registration

Tuesday, April 19

8:00–8:30	Registration/Coffee/Light Breakfast
8:30–8:45	Welcome and Kickoff
8:45–10:00	Overview of the five COEs
10:15–11:45	NVIDIA NDA session
11:45–1:15	Lunch (on your own)
1:15–3:15	Intel NDA session
3:15–3:30	Break
3:30–5:10	Talks (application presentations, 4 @ 20 minutes/each)
	Dinner on your own

Wednesday, April 20

8:00–8:30	Coffee/Light Breakfast
8:30–10:30	Talks (performance portability abstractions, 5 @ 15-20 minutes/each)
10:30–10:45	Break
10:45–12:00	Talks (application teams experience with abstractions, 4-5 @ 15 minutes/each)
12:00–1:00	Working lunch (provided)
1:00–2:20	Compiler/tools talks (4 @ 20 min/each)
2:20–3:40	Application talks (focus on compiler/tool issues, 4 @ 20 min/each)
3:45–5:15	BREAKOUT #1 (either compilers/languages or portable abstractions
	Dinner on your own

Thursday, April 21

8:00–8:30	Coffee/Light Breakfast
8:30–9:00	Recap of Breakout #1
9:00–10:00	Talks (Programming the memory hierarchy)
10:00–10:15	Break
10:15–12:00	BREAKOUT #2 (either memory hierarchy management or tools requirements)
12:00–1:00	Working lunch (provided)
1:30–2:50	Talks (applications, 4 @ 20 minutes/each)
2:50–3:20	Recap of Breakout #2
3:20–3:30	Break
3:30–4:15	"Lighting round talks" (signup at meeting for 5-minute slots on any topic)
4:15–5:15	Wrap up: next steps and takeaways
6:30–8:30	Hosted Dinner (provided)